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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>																															
<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th colspan="4" style="text-align: center; padding: 5px;">Complete If Known</th> </tr> <tr> <th style="width: 25%;">Application Number</th> <td colspan="3" style="width: 75%;">10/750,582</td> </tr> <tr> <th>Filing Date</th> <td colspan="3">December 29, 2003</td> </tr> <tr> <th>First Named Inventor</th> <td colspan="3">Tsai, Vicki</td> </tr> <tr> <th>Art Unit</th> <td colspan="3">2128</td> </tr> <tr> <th>Examiner Name</th> <td colspan="3">Ferris III, Fred O</td> </tr> </thead> <tbody> <tr> <td colspan="4" style="height: 40px;"></td> </tr> </tbody> </table>				Complete If Known				Application Number	10/750,582			Filing Date	December 29, 2003			First Named Inventor	Tsai, Vicki			Art Unit	2128			Examiner Name	Ferris III, Fred O						
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Sheet	1	of	2																												
Attorney Docket No: 80107.095US1																															

US PATENT DOCUMENTS					
Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
3		US-20040194048A1	09/30/2004	Arnold, J. M.	03/31/2003
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Examiner Initials*	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T*
3		WO-05094016A2	10/06/2005	Honary, H., et al.	
3		WO-2005094016A3	10/06/2005	Honary, H.	

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials*	Cite No *	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T*
3		<u>International Search Report and Written Opinion of the International Searching Authority; Dated 12/06/2005; PCT/US2005/010387, 16 pgs.</u>			
3		<u>ALBANESI, M. G., et al., "A VLSI 128-Processor Chip for Multiresolution Image Processing", Proceedings of the First International Conference on Ischia, (May 02, 1994), 296-307</u>			
3		<u>MARESCAUX, T., et al., "Networks on Chip as Hardware Components of an OS for Reconfigurable Systems", FIELD-PROGRAMMABLE LOGIC AND APPLICATIONS, 13TH INTERNATIONAL CONFERENCE, Vol. 2778, (2003), 595-605</u>			

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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

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		ZHANG, et al., "Interconnect Architecture Exploration for Low-Energy Reconfigurable Single-Chip DSPs", VLSI '99, PROCEEDINGS, IEEE COMPUTER SOCIETY WORKSHOP ON ORLANDO, FL, (April., 08, 1999), 2-8	

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